

Bad sector file held on surface 1 (Lower)
cylinder 255.

16 consecutive misreads flags a bad sector.
Sectors offset, top to bottom by 17. If sector
is read by time heads are changed & settled
it will be over ϕ on surface 1.

Pack Ser. no. also on cyl. 255.

Servo info is written .04" apart on track
centre lines & $\frac{1}{2}$ way between track centre lines

[#] S1 & S2 bursts used for track positioning.

Rom handles info. a) Where we were
b) Where we are.
c) Velocity F/B from tachometer.

S1 & S2 bursts used for track counting.

Servo on tracks are twice as many + than ~~data~~
track centre lines.

On TK ϕ centre line we have \bar{S}_2
Between TK ϕ & 1 we have \bar{S}_1

On TK 1 centre line we have S2
Between TK 1 & 2 we have S1 & so on.

Outer perimeter of disk has guard band of 24 tracks
of \bar{S}_1 bursts.

Inner guard band consists of 17 tracks of S2 bursts.

I.C. chips req'd. a) one straight, b) one with 6 & 7 connected
& flylead with cross dip c) one with 3 & 7 connected.

RLVII

SB # 58 - TEC. TIP VECTOR CHANGED 330 → 160
SB # 72 - FCR ? (COPY MISSING IN TRAINING),
RL01.

SB # 40 - HEAD CABLE RETAINING ASSY NOT ISOLATED FROM
R/W MODULE COVER (TT).

SB # 58 - X ref to RL3-A Boot (TT).

SB # 63 - FCR, FCO #1 ON RL11

SB # 70 - Top cover switch & brushes home switch
becoming loose on mountings (TT).

SB # 73 - Drive #'s < 313 60Hz only (TT)

RL8A

SB # 58 Errors under AJRLEAD [PERF EXERCISER] M8430-C30
should be 22 μ Fd (TT).

RL11

SB # 52 - Vector changed, diagnostic fill count problem
SB # 73 - FCO to stop being dropped by DECX11 (TT).

ZRLA → F.

SB # 71 APT Compatibility (DECO/DEPO) REV.B.

RLØ1 Problem

With any seek time out we get Fault condition & servo disabled hence, if fault is causing disk to slow down to a halt we have a head crash condition. eg. If belt comes off.