

BASE = MF-01
 DECNIS_TMSROM
 23-001MF-00

[DIGL SIG]
 [PROCSSR]
 TMS320C25
 E11

- D15 ↔ 3 JVS1 TD<15> H/I
- D14 ↔ 4 JVS1 TD<14> H/I
- D13 ↔ 5 JVS1 TD<13> H/I
- D12 ↔ 6 JVS1 TD<12> H/I
- D11 ↔ 7 JVS1 TD<11> H/I
- D10 ↔ 8 JVS1 TD<10> H/I
- D9 ↔ 9 JVS1 TD<9> H/I
- D8 ↔ 11 JVS1 TD<8> H/I
- D7 ↔ 11 JVS1 TD<7> H/I
- D6 ↔ 12 JVS1 TD<6> H/I
- D5 ↔ 13 JVS1 TD<5> H/I
- D4 ↔ 14 JVS1 TD<4> H/I
- D3 ↔ 15 JVS1 TD<3> H/I
- D2 ↔ 17 JVS1 TD<2> H/I
- D1 ↔ 18 JVS1 TD<1> H/I
- D0 ↔ 18 JVS1 TD<0> H/I

- A15 ↔ 43 JVS1 TA<15> H/I
- A14 ↔ 42 JVS1 TA<14> H/I
- A13 ↔ 41 JVS1 TA<13> H/I
- A12 ↔ 40 JVS1 TA<12> H/I
- A11 ↔ 39 JVS1 TA<11> H/I
- A10 ↔ 38 JVS1 TA<10> H/I
- A9 ↔ 37 JVS1 TA<9> H/I
- A8 ↔ 36 JVS1 TA<8> H/I
- A7 ↔ 33 JVS1 TA<7> H/I
- A6 ↔ 32 JVS1 TA<6> H/I
- A5 ↔ 31 JVS1 TA<5> H/I
- A4 ↔ 30 JVS1 TA<4> H/I
- A3 ↔ 29 JVS1 TA<3> H/I
- A2 ↔ 29 JVS1 TA<2> H/I
- A1 ↔ 28 JVS1 TA<1> H/I
- A0 ↔ 26 JVS1 TA<0> H/I

- FSX ↔ 54 JVS1 JE_TMS_DR H/I
- FSX ↔ 53 JVS1 JE_TMS_FSR H/I

- R/W ↔ 48 JVS1 TRW H/I
- STRB ↔ 49 JVS1 TRW H/I
- PS ↔ 47 JVS1 TPS L
- DS ↔ 45 JVS1 TDS L/I
- IS ↔ 46 JVS1 TDS L/I
- MSC ↔ 50
- BR ↔ 56
- XF ↔ 60
- IACK ↔ 60

- HOLDA ↔ 55
- READY ↔ 66
- BIO ↔ 68
- RS ↔ 65
- SYNC ↔ 19
- CLKOUT1 ↔ 58
- CLKOUT2 ↔ 57

- X1 ↔ 51 JVS1 TCLK1 UNTERM H
- X2 /CLKIN ↔ 52 JVS1 TCLK2 H

- CSR2 JE_TMS_DX H/I ↔ 24 DR
- CSR2 JE_TMS_FSX H/I ↔ 25 FSR
- CSR2 JE_TMS_CLKX H/I ↔ 64 CLKR
- CSR2 JE_TMS_CLKR H/I ↔ 63 CLKX

- PWR4 PDN_17 L/G ↔ 1 MP
- CSR2 JEEVES_INT L/I ↔ 22 MC
- JVS9_TMS_INT<1> L ↔ 21 INT2
- TOP2_TINT0 L/I ↔ 20 INT1

- PWR5 PUP_17 H/G ↔ 67 HOLD
- JVS14 TRDY H/I ↔ 66 READY
- PWR5 PUP_18 H/G ↔ 68 BIO
- TOP2_TRST L/I ↔ 65 RS
- TOP2_TSYNC L/I ↔ 19 SYNC

- TOP2_TMS_40MHZ H/I ↔ 52

21-26307-15

[32KX8]
 [SRAM]
 MCM6206
 E10

- AV ↔ 19 JVS1 TD<15> H/I
- ↔ 18 JVS1 TD<14> H/I
- ↔ 17 JVS1 TD<13> H/I
- ↔ 16 JVS1 TD<12> H/I
- ↔ 15 JVS1 TD<11> H/I
- ↔ 13 JVS1 TD<10> H/I
- ↔ 12 JVS1 TD<9> H/I
- ↔ 11 JVS1 TD<8> H/I

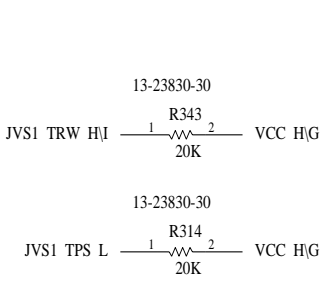
SRAM
 32KX8

- JVS1 TA<14> H/I ↔ 1
- JVS1 TA<13> H/I ↔ 26
- JVS1 TA<12> H/I ↔ 2
- JVS1 TA<11> H/I ↔ 23
- JVS1 TA<10> H/I ↔ 21
- JVS1 TA<9> H/I ↔ 24
- JVS1 TA<8> H/I ↔ 25
- JVS1 TA<7> H/I ↔ 3
- JVS1 TA<6> H/I ↔ 4
- JVS1 TA<5> H/I ↔ 5
- JVS1 TA<4> H/I ↔ 6
- JVS1 TA<3> H/I ↔ 7
- JVS1 TA<2> H/I ↔ 8
- JVS1 TA<1> H/I ↔ 9
- JVS1 TA<0> H/I ↔ 10

- JVS8 SRAM_OE L ↔ 22
- JVS8 SRAM_SEL L ↔ 20
- JVS8 SRAM_WE L ↔ 27

1
 32K
 [BT]

- EN[OUTPUT]
- CS
- IC2 [WRITE]
- IEN [READ]



21-26307-15

[32KX8]
 [SRAM]
 MCM6206
 E4

- AV ↔ 19 JVS1 TD<7> H/I
- ↔ 18 JVS1 TD<6> H/I
- ↔ 17 JVS1 TD<5> H/I
- ↔ 16 JVS1 TD<4> H/I
- ↔ 15 JVS1 TD<3> H/I
- ↔ 13 JVS1 TD<2> H/I
- ↔ 12 JVS1 TD<1> H/I
- ↔ 11 JVS1 TD<0> H/I

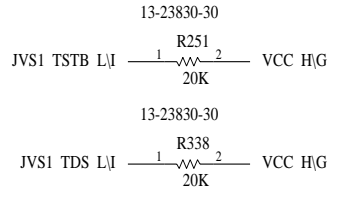
SRAM
 32KX8

- JVS1 TA<14> H/I ↔ 1
- JVS1 TA<13> H/I ↔ 26
- JVS1 TA<12> H/I ↔ 2
- JVS1 TA<11> H/I ↔ 23
- JVS1 TA<10> H/I ↔ 21
- JVS1 TA<9> H/I ↔ 24
- JVS1 TA<8> H/I ↔ 25
- JVS1 TA<7> H/I ↔ 3
- JVS1 TA<6> H/I ↔ 4
- JVS1 TA<5> H/I ↔ 5
- JVS1 TA<4> H/I ↔ 6
- JVS1 TA<3> H/I ↔ 7
- JVS1 TA<2> H/I ↔ 8
- JVS1 TA<1> H/I ↔ 9
- JVS1 TA<0> H/I ↔ 10

- JVS8 SRAM_OE L ↔ 22
- JVS8 SRAM_SEL L ↔ 20
- JVS8 SRAM_WE L ↔ 27

1
 32K
 [BT]

- EN[OUTPUT]
- CS
- IC2 [WRITE]
- IEN [READ]



..DRAWING

JEEVES
 JEEVES

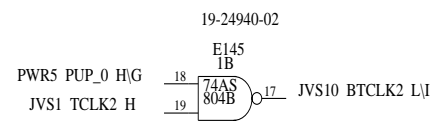
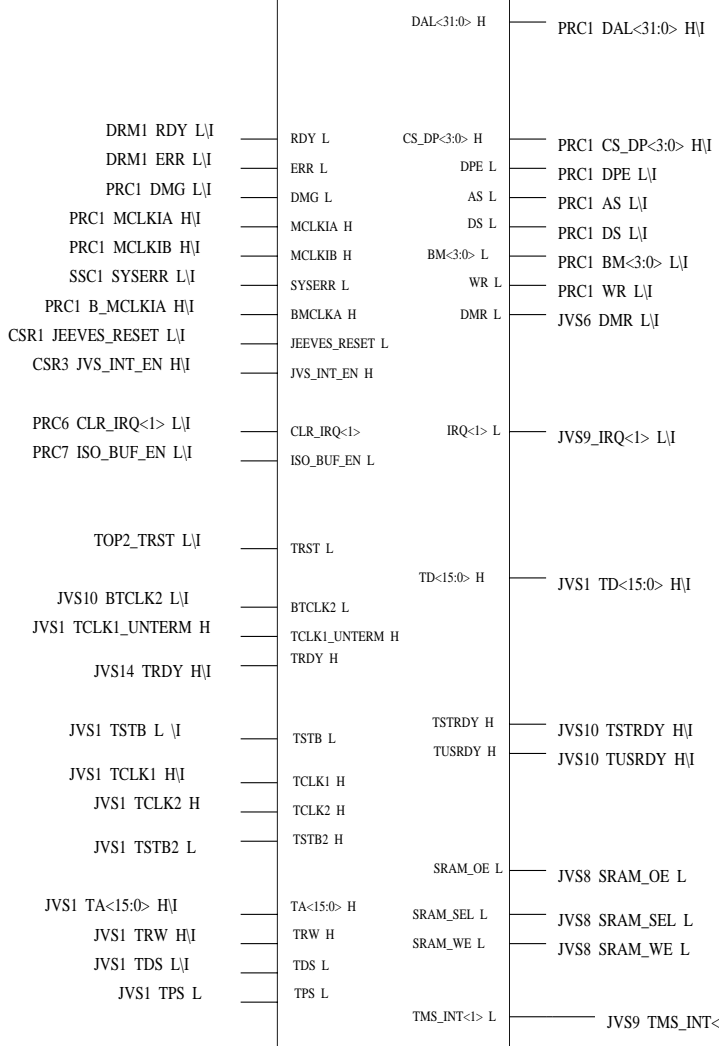
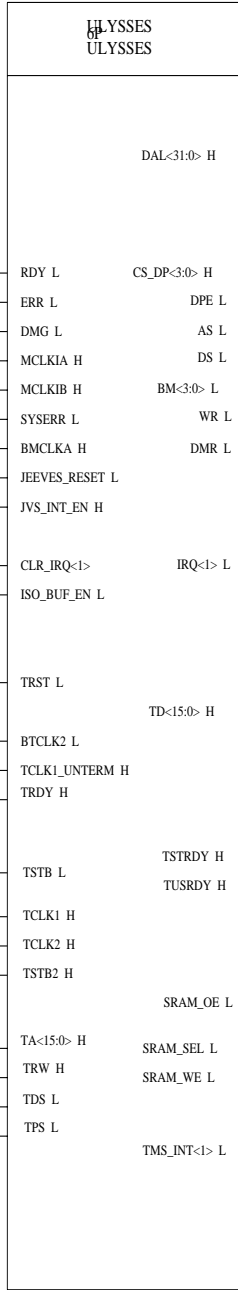
Thu Nov 9 16:03:53 1995

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REVISION	CHK	CHANGE NO.	REV

digital	DRN	DATE	ENG.	DATE	TITLE:
	JOHN BELL	18-OCT-95	FRASER MURPHY	25-OCT-95	JEEVES
USRA:	CHK'D.	DATE	BOARD LOCATION:	SHEET	TOP DOCUMENT NUMBER:
FIRST USED ON OPTION MODEL: D5NSAN	JOHN BELL	25-OCT-95		1 OF 2	K-DD-B1000-A-C

MEDIA	CODE	NUMBER	REV.
K	CS	B1000-A-C-JVS	AX01



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REVISION	
CHK	CHANGE NO. REV

digital
USRA:
FIRST USED ON OPTION MODEL- DNSAN

DRN JOHN BELL	DATE 18-OCT-95
CHK'D JOHN BELL	DATE 25-OCT-95

ENG. FRASER MURPHY	DATE 25-OCT-95
BOARD LOCATION: SHEET	2 OF 2
TOP DOCUMENT NUMBER: K-DD-B1000-A-C	

TITLE: JEEVES	NUMBER B1000-A-C-JVS	REV. AX01
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AX01
B1000-A-C-JVS
CS
K
B