Cache Tag Store

PLACE SERIES RESISTORS CLOSE TO THE SRAMs
Cache Data Store
Ownership RAMS
ANALYSER CONNECTORS FOR NDAL

NOTE: REMOVED NDAL ARB PINS FROM TEST CONNECTOR
MEMORY SIMM DECOUPLING

SIMM A

C130: 22μF 15V
C131: 1μF 25V
C134: 1μF 50V
C309: 1μF 25V
C305: 1μF 25V
C325: 1μF 25V
C319: 1μF 25V
C318: 1μF 25V
C313: GND

SIMM B

C112: 22μF 15V
C113: 1μF 25V
C302: 1μF 25V
C301: 1μF 25V
C315: 1μF 25V
C321: 1μF 50V
C316: 1μF 25V
C317: 1μF 25V
C311: GND
CP BUS Clocks, Oscillators

NOTES:
1. MCLK SERIES RESISTORS (NCA) TO BE PLACED AT TEE NEAR CCLK
2. DO NOT SHORT OR BACKDRIVE THE OSC_H OR OSC_L SIGNALS.

Path from the Oscillator to NVAX must be balanced within 25 mils.
Place these caps close to the oscillator pins!
ANALYSER CONNECTORS FOR CP2BUS
The Nx_PHixx lines must be equal length.
The UND_PHixx lines must be equal length.

NOTE:
The UND_PHixx lines must be equal length.
CP BUS Pullup Resistors
MEMORY CONTROL PAL

NOTE: PINS 1 & 11 ARE IO & I9 NOT OEL & CLK
SIMM CONNECTORS