FIELD CHANGE ORDER
Number: TQK50-I001

Applicability: All TQK50 Q bus controller modules (M7546) below Rev F1 when problems are evident. All 1123 and 1153 systems must have this installed to operate under RSTS 9.2.

Problem/Symptom: 1123 and 1153 systems, 1. Memory corruption due to bus grant problem. 2. 64K page boundary crossing problem under DECX. 3. Bus interaction with 16 bit backup. uVAX Systems, 1. Not software enabled messages. 2. Phantom device address of MUA0 4224.

Quick Check: Chip in position E3 is marked 065L1.

Compatibility/Prerequisite FCO:
N/A

Est. Time to Install: .5 hours

Special Tools or Test Equipment: N/A

Order by FCO Kit#:
EQ-01442-01 1 KIT
FA-04728-01 1 FCO Document

EQ Kit Variation System/Option Applic: N/A

Approvals

CSSE Engineer
Bob Steere

F.S. Product Safety
Steve Caccia

F.S. Logistics
Fred Saunders

CSSE Manager
Steve Dail

F.S. Microfiche Libraries
EP-FSNVX-LB VAX
EP-FSP11-LB PDP11

Affected Population:
6,000

ESD&P Micropub.
Ray leBlanc

Initial Kitting:
2,000
The minimum shippable revision of this module from all F.S. locations is F1 as of July 18, 1986.

**********************************************************
*          C A U T I O N                       *
* The M7546 module contains electrostatic discharge  *
* sensitive devices (ESDS). The use of the VELOSTAT  *
* kit is essential to prevent damage which may not be  *
* noticed immediately.                                  *
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INSTALLATION PROCEDURE:

1. Perform normal system shutdown procedures.

2. Power down system.

3. Remove interface cable from M7546 module, P1.

4. Remove module M7546 from backplane.

5. Using figure 1 as a reference, replace the following components with parts supplied.

   A. Remove chip from position E3, replace with chip marked 23-065L1-00.

   B. Remove chip from position E28, replace with chip marked 23-249E5-00.

   C. Remove chip from position E29, replace with chip marked 23-248E5-00.

   NOTE: This ROM replacement brings the microcode (FW) to REV 4, and the hardware (HW) REV to 6.

6. Using figure 1 as a reference, change switch setting of component E38 to: bits 2 and 3 on, all others off. This changes the hardware REV register to 6.
7. Mark the hande REV Fl.


9. Power up system.

10. Observe for normal power up onboard diagnostic completion. Refer to paragraph 4.4.1.7 and 4.4.1.8, page 4-12 in the TK50 Tape Drive Subsystem Manual, # EK-0TK50-TM.

11. Return system to customer.

12. Complete LARS form as per Figure 2.

Figure 1

LARS

<table>
<thead>
<tr>
<th>Activity</th>
<th>DEC</th>
<th>Type</th>
<th>Action</th>
<th>FCO #</th>
<th>Material Used</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
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For Contract and Warrant Customers U.S. and G.I.A.

W TQK50 M D TQK50-I001 EQ-01442-01